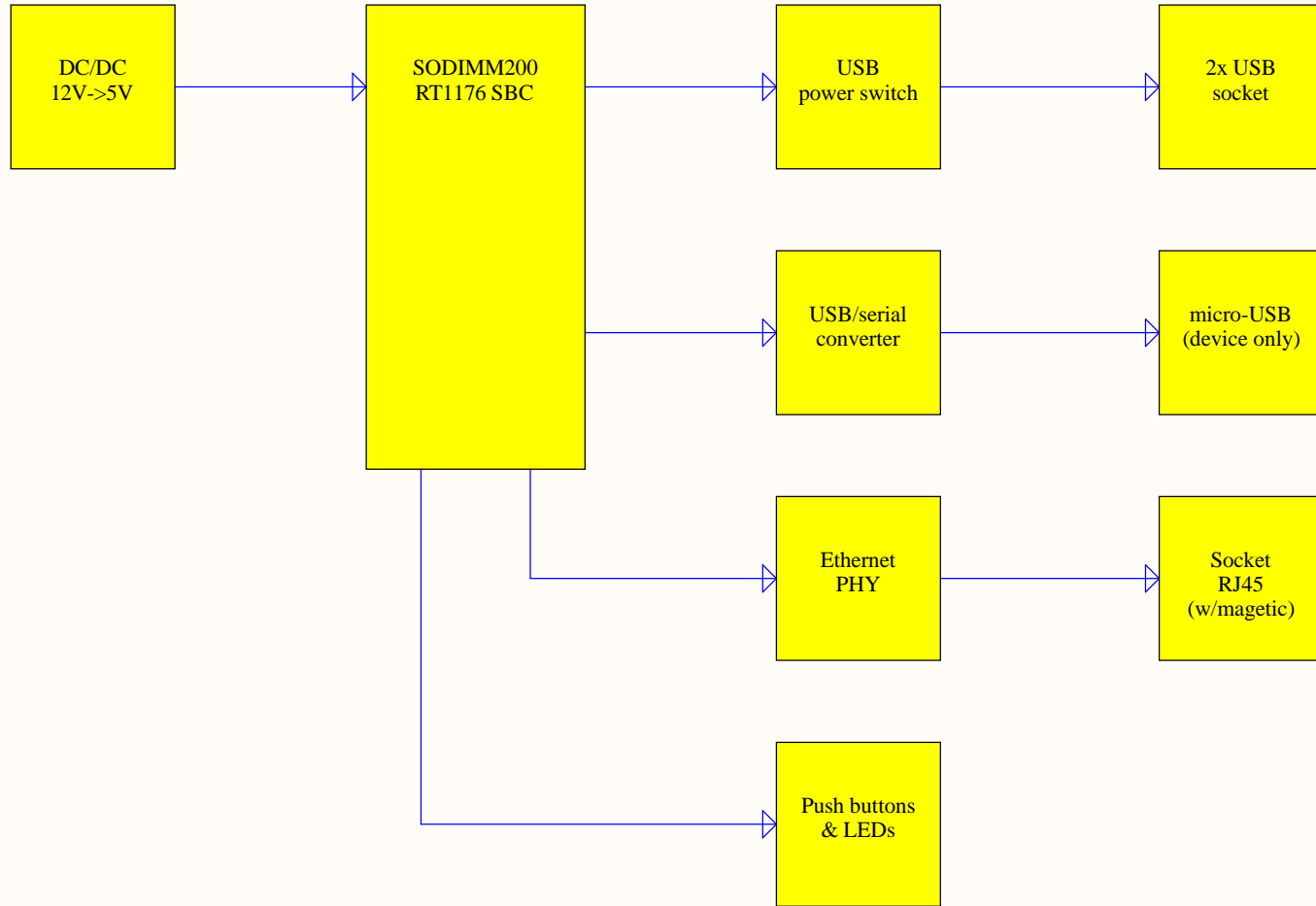


# Block diagram



U\_02\_PowerTree  
02\_PowerTree.SchDoc

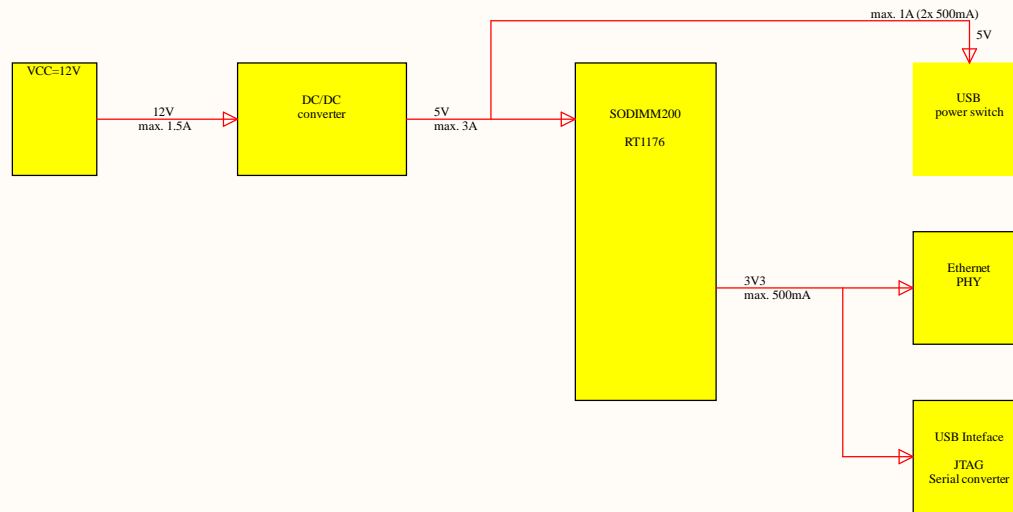


U\_03\_Interconnect  
03\_Interconnect.SchDoc

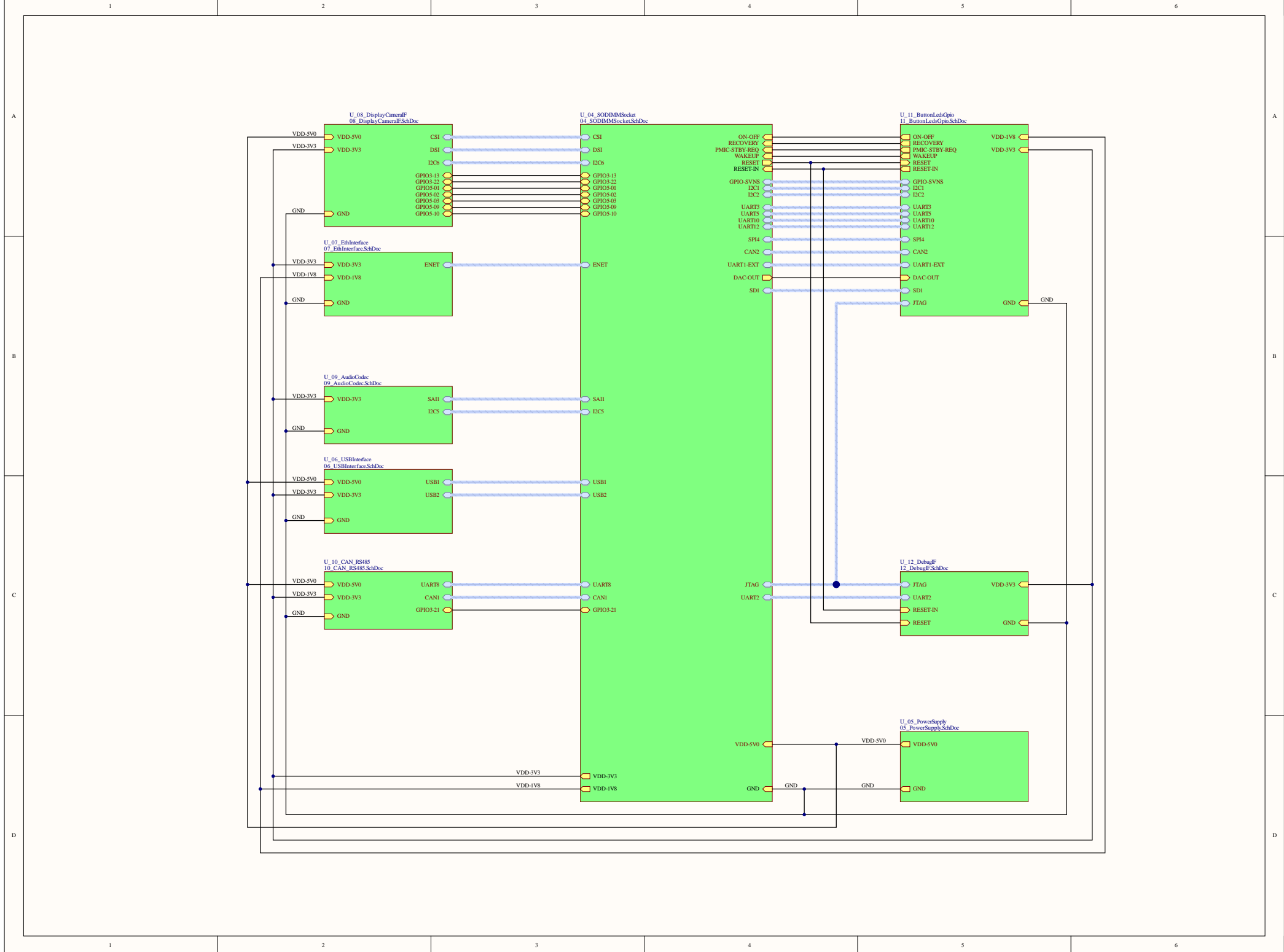


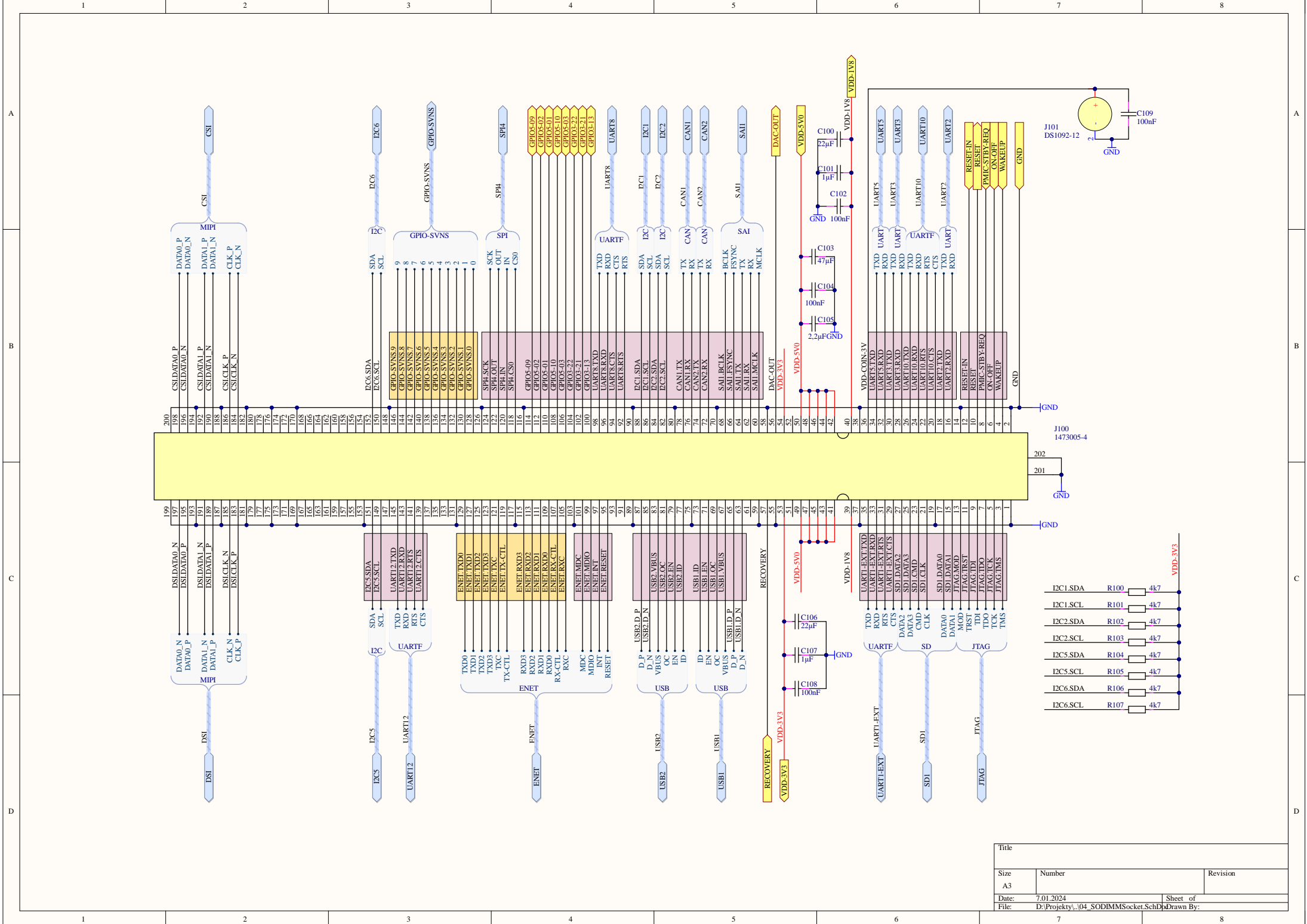
Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\...01_BlockDiagram.SchDoc	Drawn By:

# Power tree



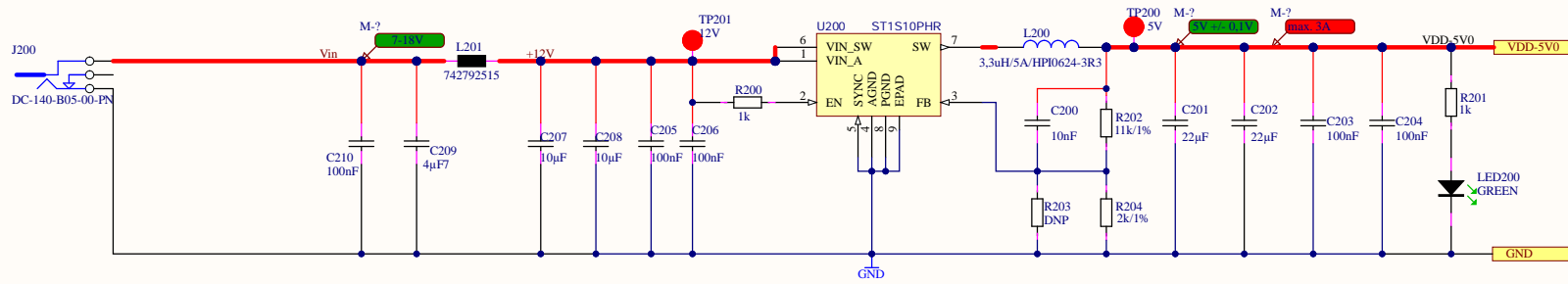
Title		
Size A3	Number	Revision
Date: 7.01.2024	Sheet of	
File: D:\Projekty\...02_PowerTree.SchDoc	Drawn By:	





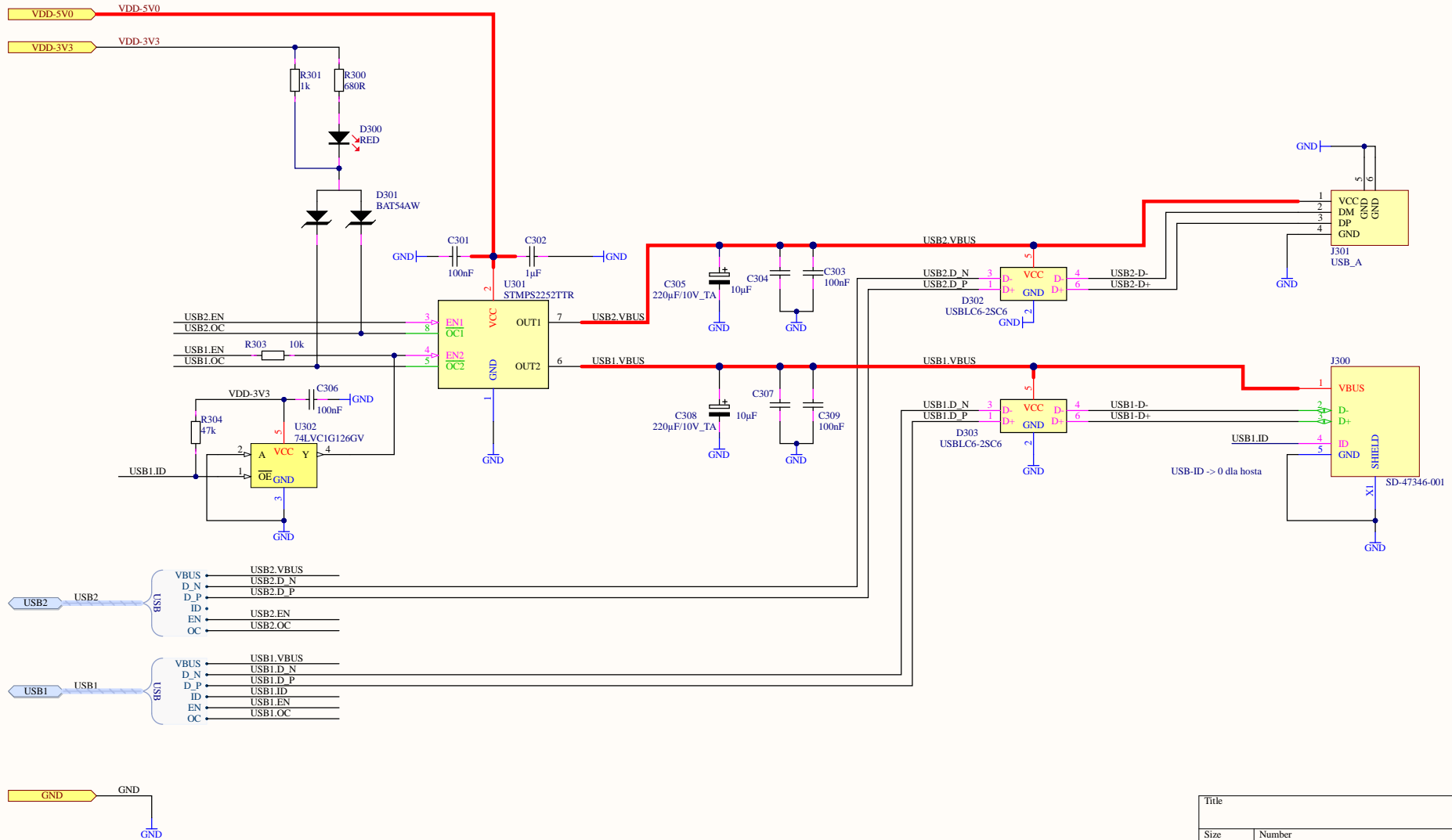
Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\104_SODIMMSocket\SchD	Drawn By:

# Power supply - DC/DC converter



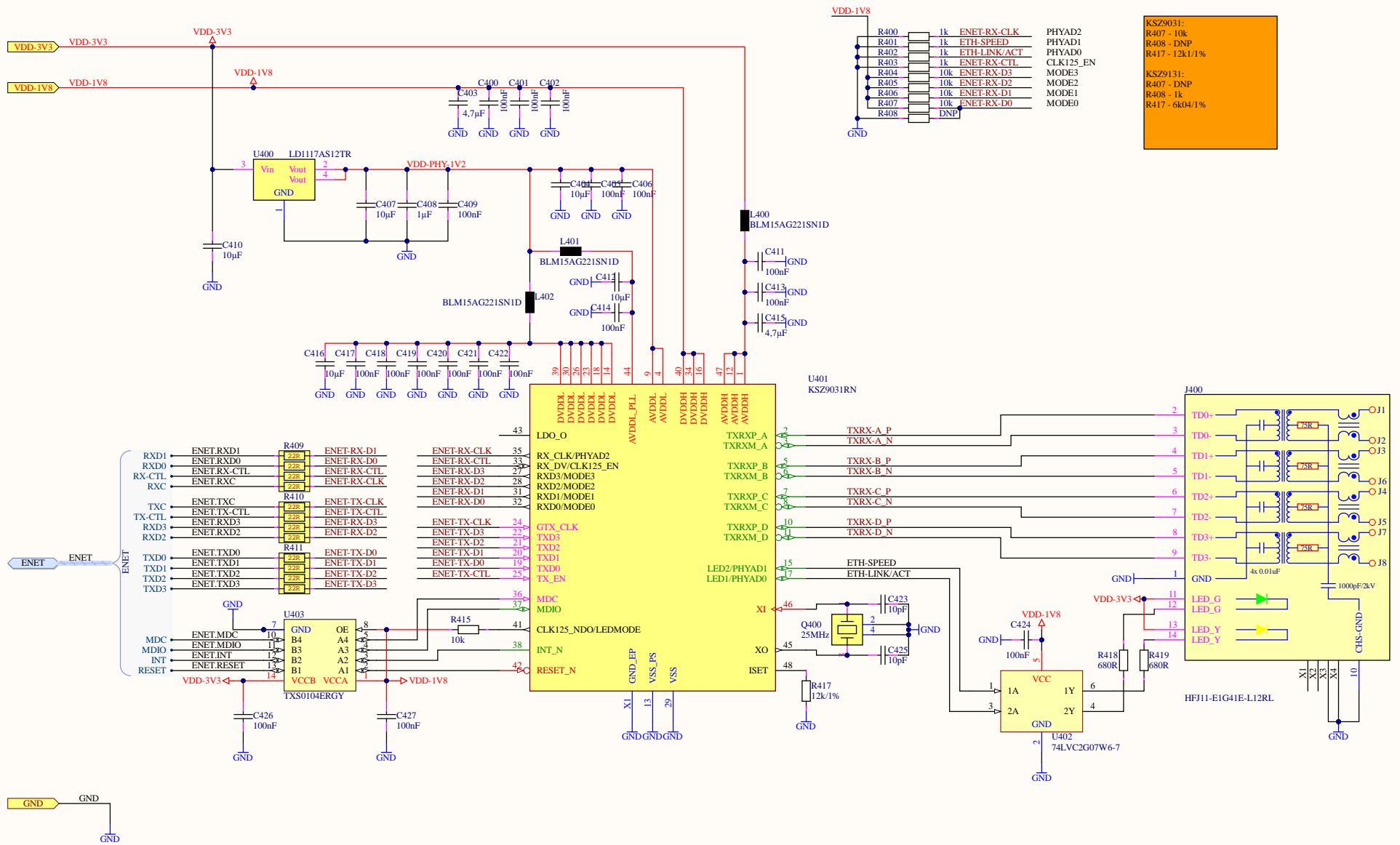
Title		
Size A3	Number	Revision
Date: 7.01.2024	Sheet of	
File: D:\Projekt\...05_PowerSupply.SchDoc	Drawn By:	

# USB interfaces



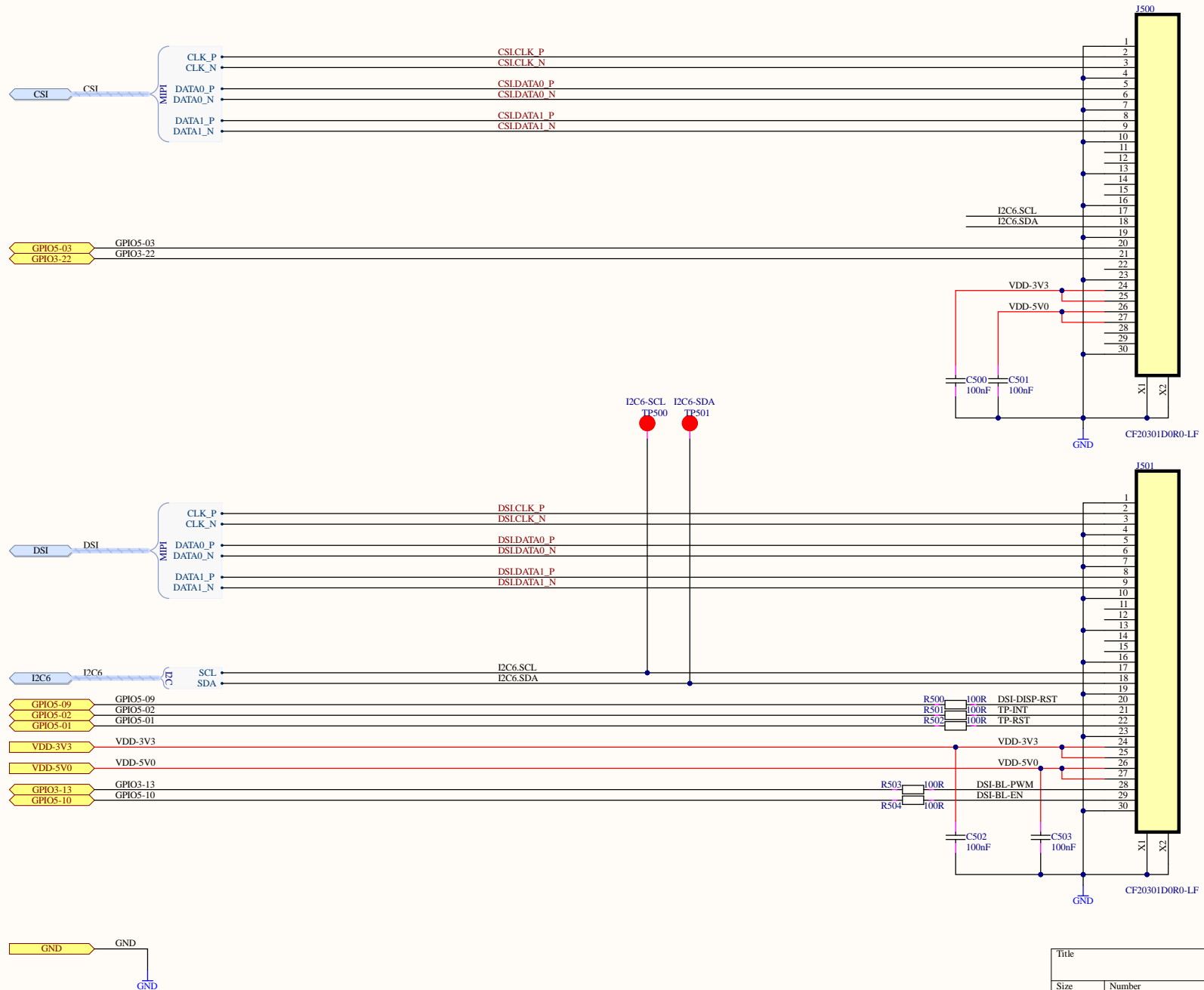
Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekt\...06_USBInterface.SchDoc	Drawn By:

# Gigabit Ethernet



Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\07_EthInterface.SchDoc	Drawn By:

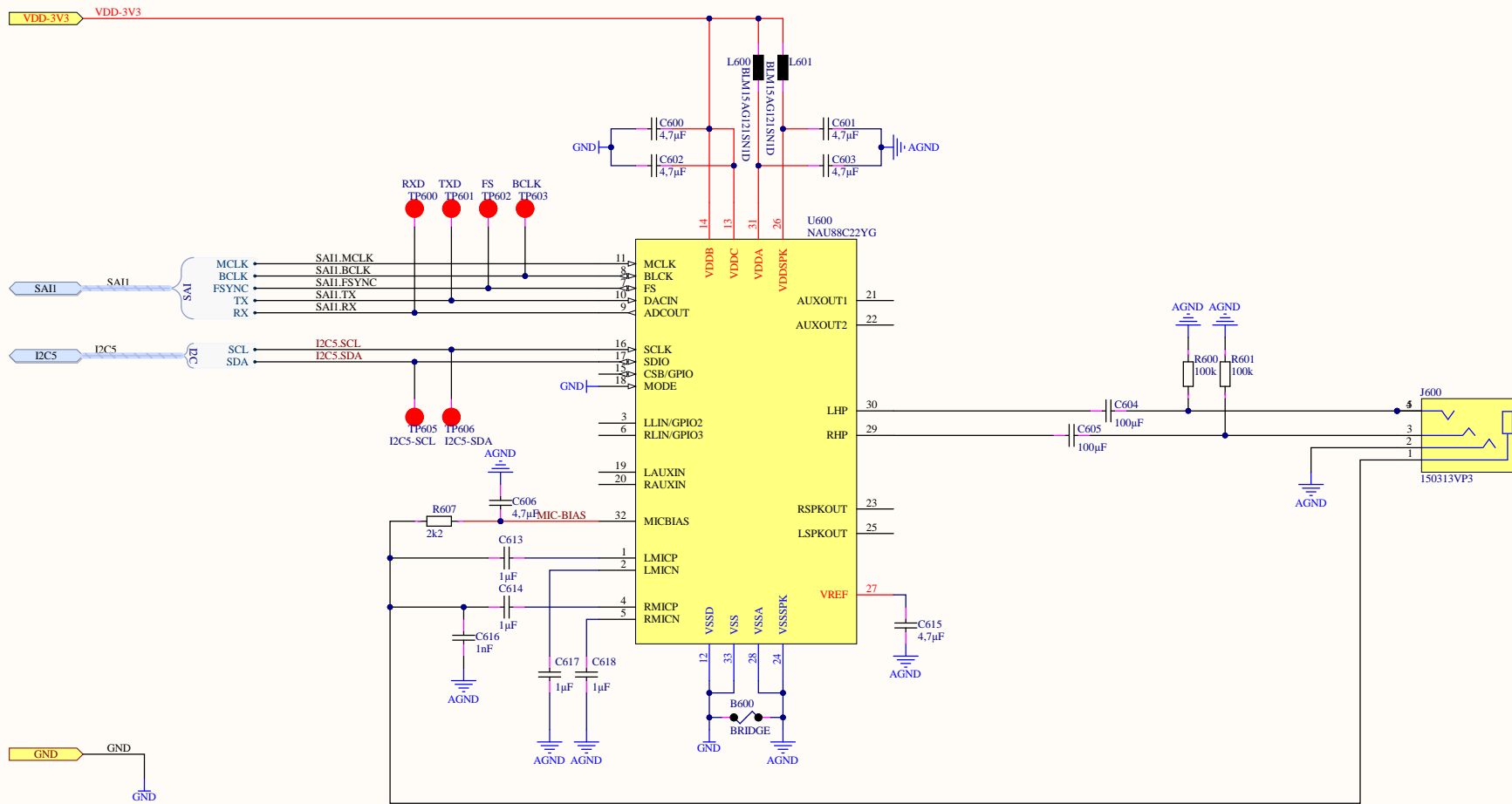
# Camera & Display IF



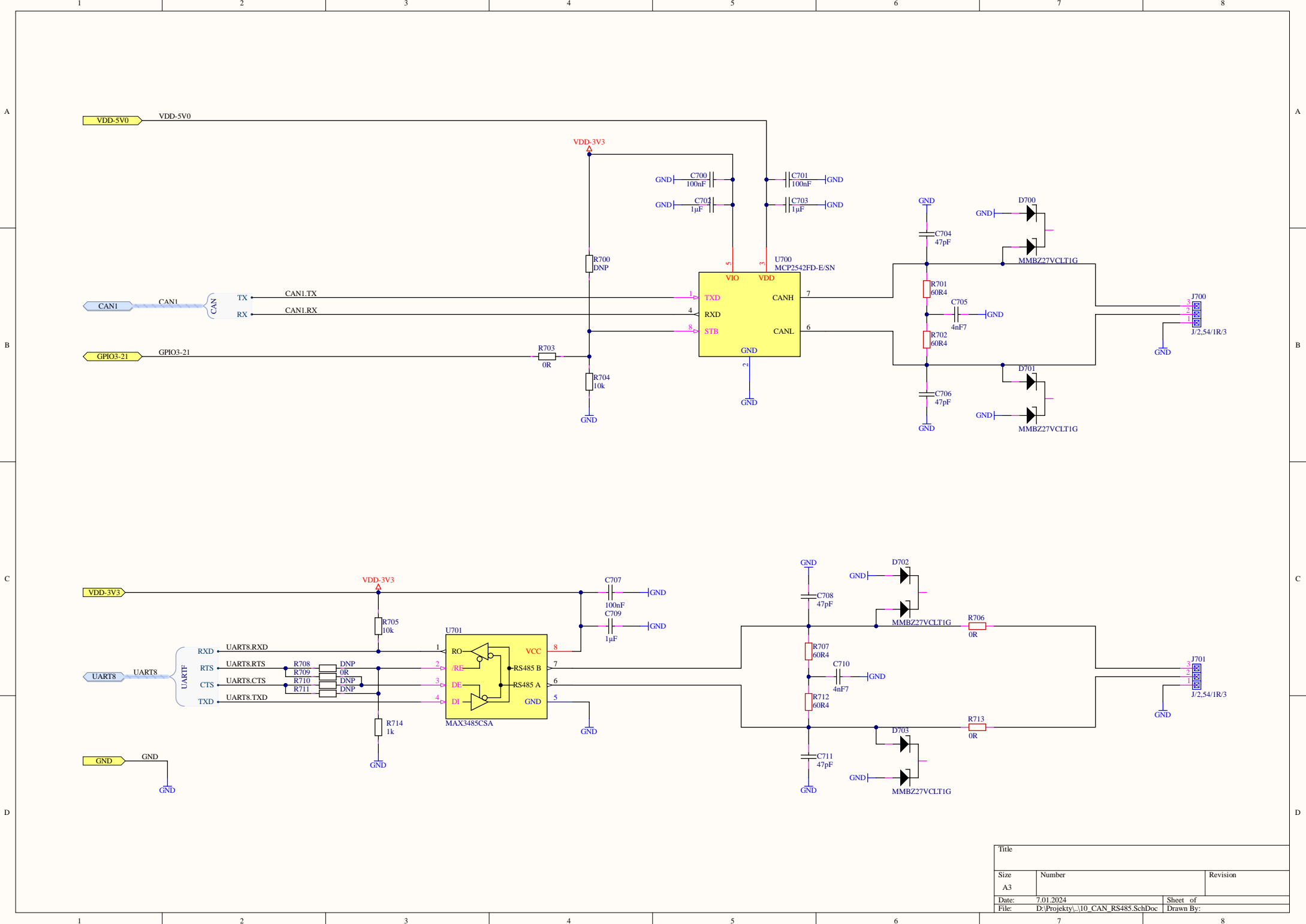
Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\08_DisplayCamera\Fsch\	Drawn By:



# AUDIO-CODEC



Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekt\...09_AudioCodec.SchDoc	Drawn By:



Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\10_CAN_RS485.SchDoc	Drawn By:

VDD-1V8 VDD-1V8  
VDD-3V3 VDD-3V3

GPIO-SVNS GPIO-SVNS

I2C1 I2C1  
I2C2 I2C2  
UART3 UART13  
UART5 UART15  
UART10 UART10  
UART12 UART12  
CAN2 CAN2  
SPI4 SPI4  
JTAG JTAG

UART1-EXT UART1-EXT

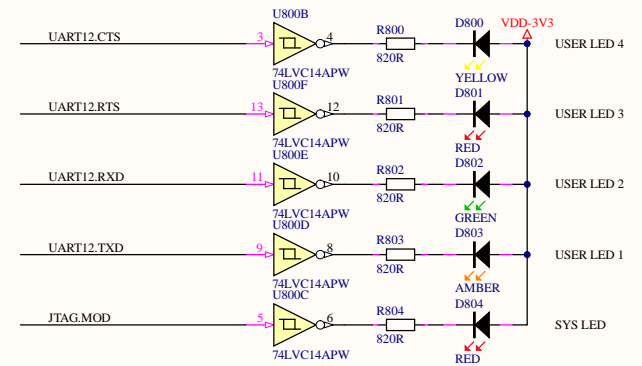
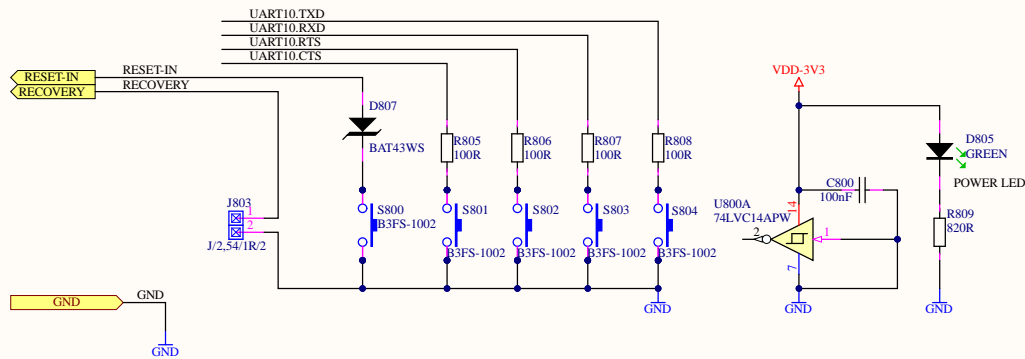
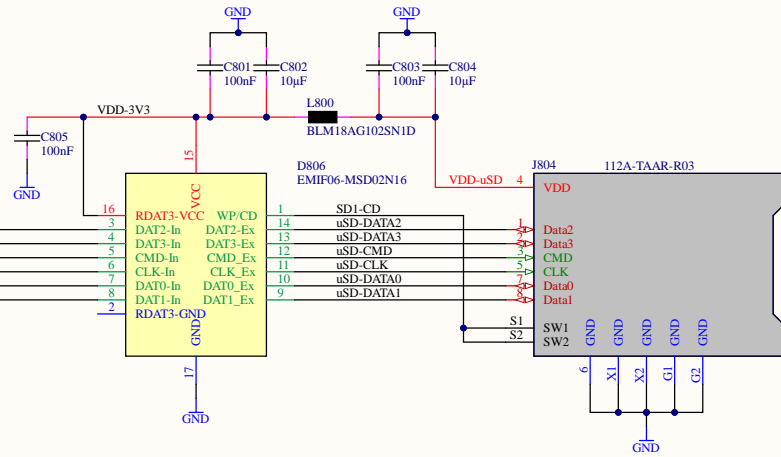
SD1 SD1

DAC-OUT DAC-OUT  
RESET RESET  
ON-OFF ON-OFF  
WAKEUP WAKEUP  
PMIC-STBY-REQ PMIC-STBY-REQ

J800  
GPIO-SVNS.9 3  
GPIO-SVNS.7 5  
GPIO-SVNS.5 6  
GPIO-SVNS.3 8  
GPIO-SVNS.1 9  
PMIC-STBY-REQ 11  
WAKEUP 12  
VDD-1V8 15  
GPIO-SVNS.8 4  
GPIO-SVNS.6 7  
GPIO-SVNS.4 10  
GPIO-SVNS.2 11  
GPIO-SVNS.0 12  
RESET 13  
ON-OFF 14  
GND 16  
J/2.54/2R/16  
GND

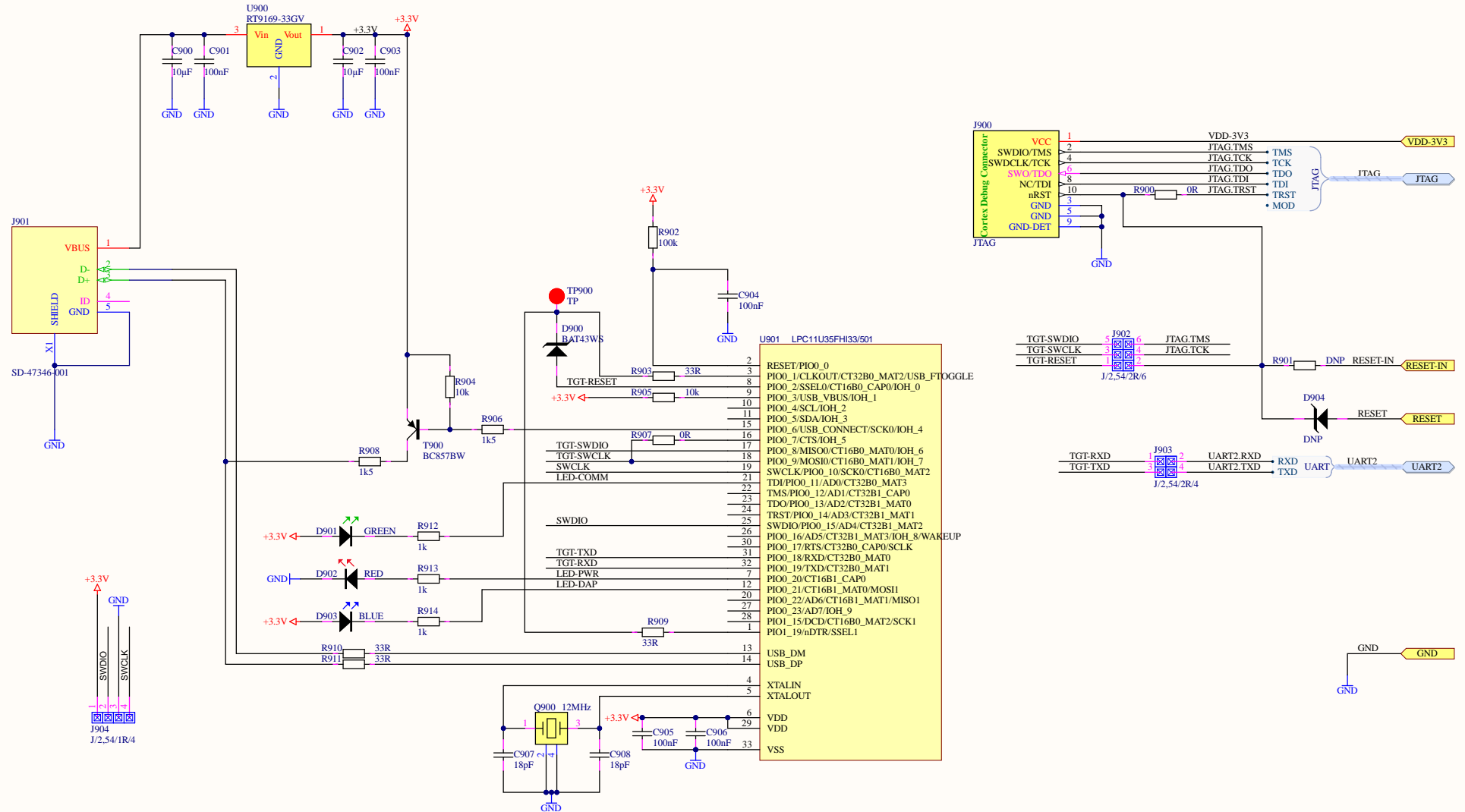
J801  
UART5.TXD 1  
UART5.RXD 3  
UART10.TXD 5  
UART10.RXD 7  
UART10.RTS 9  
UART10.CTS 11  
VDD-3V3 15  
UART3.TXD 2  
UART3.RXD 4  
UART12.TXD 6  
UART12.RXD 8  
UART12.RTS 10  
UART12.CTS 12  
DAC-OUT 14  
GND 16  
J/2.54/2R/16  
GND

J802  
SPI4.SCK 1  
SPI4.IN 3  
I2C1.SDA 5  
I2C1.SCL 6  
I2C2.SDA 7  
CAN2.TX 9  
UART1-EXT.RXD 11  
UART1-EXT.CTS 13  
VDD-3V3 15  
SPI4.OUT 2  
SPI4.CS0 4  
I2C1.SCL 6  
I2C2.SCL 8  
CAN2.RX 10  
UART1-EXT.TXD 12  
UART1-EXT.RTS 14  
GND 16  
J/2.54/2R/16  
GND



Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\11_ButtonLedsGpio.SchDok	Drawn By:

# Debug IF/Serial Console



Title		
Size	Number	Revision
A3		
Date:	7.01.2024	Sheet of
File:	D:\Projekty\12_DebugIF\SchDoc	Drawn By: